



Brennan 2-81-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): J.M. Brennan et al.

Case: 2-81-1-4

Serial No.: 10/773,614

Filing Date: February 6, 2004

Group: 2823

Examiner: Brook Kebede

Title: Semiconductor Device Configured for
Reducing Post-Fabrication Damage

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service
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Sir:

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In response to the final Office Action dated December 28, 2005, Applicants submit
herewith the following Amendment for entry in the above-identified application: